EE-401-E **DATA COMMUNICATION**

L T P			
3 1 0	CLASS WORK	:	50
	EXAM	:	100
	TOTAL	:	150
	DURATION OF EXAM	:	3 HRS

UNIT 1 DIGITAL COMMUNICATION :
Introduction, digital communication, Shannon limit for information capacity, digital radio, digital amplitude modulation, frequency shift keying (FSK), phase shift keying (PSK), quadrature amplitude modulation (QAM), band width efficiency, carrier recovery, differential phase shift keying,(DPSK), clock recovery, probability of error & bit error rate, trellis encoding.

UNIT 2 DATA COMMUNICATIONS:
Introduction, history of data communication, standard organization for data communication, data communication circuits, data communication codes, error control, synchronization, data communications hardware, serial interfaces: RS-232, RS-449 & RS-530, CCITT X.21, parallel interfaces: centronics parallel interfaces. the telephone network: DDD network, private- line service, the telephone circuit, data modems: synchronous modems, asynchronous modems, modem synchronization.

UNIT 3 DATA COMMUNICATIONS PROTOCOLS AND NETWORK CONFIGURATIONS :
Introduction, open system interconnection (OSI), data transmission mode, asynchronous protocols, synchronous protocols, public data network, integrated services digital network (ISDN), local area networks, token pass ring, Ethernet.

UNIT 4 MULTIPLEXING :
Introduction, time division multiplexing, T1 digital carrier system, CCITT time division multiplexed carrier systems, CODECS, COMBO chips, line encoding, T-CARRIERS, frame synchronization, bit interleaving VS word interleaving, frequency division multiplexing, AT&T's FDM hierarchy, composite base band signal, formation of a master group.

UNIT 5 INTERNET AND TCP/IP:
Introduction, history, use of Internet, accessing the Internet, Internet addresses, security on the internet, authentication, firewalls, intranet and extranet, TCP/IP reference model, domain name service, world wide web.

TEXT BOOK:
1. Electronic Communications Systems (4^{th} Ed.) : Wayne Tomasi; Pearson
2. Data Communication and Networking (2^{nd} -edition): Forauzan;

NOTE Eight questions are to be set at-least one from each unit. Students have to attempt any five questions

IC-403-E **EMBEDDED SYSTEM DESIGN**

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Class Work : 50 Marks
Exam : 100 Marks
Total : 150 Marks
Duration of Exam : 3 Hrs.

UNIT 1 : INTRODUCTION

Different types of microcontrollers: Embedded microcontrollers, External memory microcontrollers; Processor Architectures: Harvard V/S Princeton , CISC V/S RISC; microcontrollers memory types; microcontrollers features : clocking, i/o pins, interrupts, timers, peripherals.

UNIT 2 : MICROCONTROLLER ARCHITECTURE

Introduction to PIC microcontrollers, Architecture and pipelining, program memory considerations, Addressing modes, CPU registers, Instruction set, simple operations.

UNIT 3 : INTERRUPTS AND I/O PORTS

Interrupt logic, Timer2 scalar initialization, IntService Interrupt service routine, loop time subroutine, External interrupts and timers, Synchronous serial port module, Serial peripheral device, O/p port Expansion, I/p port expansion, UART.

UNIT 4 : SOFTWARE

Development tools/ environments, Assembly language programming style, Interpreters, High level languages, Intel hex format object files, Debugging.

UNIT 5 : PROGRAMMING WITH MICROCONTROLLERS

Arithmetic operations, Bit addressing, Loop control, Stack operation, Subroutines, RAM direct addressing, state machines, Oscillators, Timer Interrupts, Memory mapped I/O.

UNIT 6 : DESINING USING MICROCONTROLLERS

Music box, Mouse wheel turning, PWM motor control, Aircraft Demonstration, ultra sonic distance measuring, Temperature Sensor, Pressure Sensor, Magnetic Field Sensor.

TEXT BOOK:

1. Design with PIC Microcontrollers by John B. Peatman , Pearson.

REFERENCE BOOKS :

1. Programming and Customizing the 8051 Microcontroller : Predko ; TMH.
2. Designing Embedded Hardware : John Catsoulis ;SHROFF PUB. & DISTR. ND.
3. Programming Embedded Systems in C and C++ : Michael Barr; SHROFF PUB. & DISTR. ND.

EE-405-E **OPTICAL COMMUNICATION SYSTEMS**

L T P			
3 1 0	CLASS WORK	:	50
	EXAM	:	100
	TOTAL	:	150
	DURATION OF EXAM	:	3 HRS

UNIT1 INTRODUCTION TO OPTICAL COMMUNICATION SYSTEMS :
Electromagnetic spectrum used for optical communication, block diagram of optical communication system. Basics of transmission of light rays. Advantages of optical fiber communication.

UNIT2 OPTICAL FIBERS:
Optical fibers structures and their types, fiber characteristics : attenuation, scattering, absorption, fiber bend loss, dispersion; fiber couplers and connectors

UNIT3. LED LIGHT SOURCE :
Light emitting diode : recombination processes, the spectrum of recombination radiation, LED characteristics, internal quantum efficiency, external quantum efficiency, LED structure, lens coupling to fiber, behavior at high frequencies.

UNIT4. LASER LIGHT SOURCE :
Basic principles of laser action in semi -conductors, optical gain, lasing threshold, laser structures and characteristics, laser to fiber coupling, comparison with LED source.

UNIT5 . AVALANCHE AND PIN PHOTODETECTORS:
Principles of optical detection, quantum efficiency, responsivity, general principles of PIN photodetector, intrinsic absorption, materials and designs for PIN photodiodes, impulse and frequency response of PIN photodiodes, noise in PIN Photodiodes, multiplication process, APD Design, APD bandwidth, APD noise.

TEXT BOOK:
Optical Fiber Communications: John M Senior; PHI.

REFERENCE BOOKS :
1. Optical Communication Systems : John Gowar; PHI.
2. Optical Fiber Communications : Gerd Keiser; TMH
3. Optical fiber Communication : Selvarajan, Kar, Srinivas; TMH.

NOTE: Eight questions are to be set at least one question from each unit. Students have to attempt five question in all.

EE-407-E **DIGITAL SIGNAL PROCESSING**

L T P			
3 1 0	CLASS WORK	:	50
	EXAM	:	100
	TOTAL	:	150
	DURATION OF EXAM	:	3 HRS

UNIT1. DISCRETE-TIME SIGNALS:
Signal classifications, frequency domain representation, time domain representation, representation of sequences by Fourier transform, properties of Fourier transform, discrete time random signals, energy and power theorems.

UNIT2. DISCRETE-TIME SYSTEMS : Classification, properties, time invariant system, finite impulse Response (FIR) system, infinite impulse response (IIR) system.

UNIT3. SAMPLING OF TIME SIGNALS:
Sampling theorem, application, frequency domain representation of sampling, reconstruction of band limited signal from its samples. discrete time processing of continuous time signals, changing the sampling rate using discrete time processing.

UNIT4. Z-TRANSFORM :
Introduction, properties of the region of convergence, properties of the Z-transform, inversion of the Z-transform, applications of Z-transform.

UNIT5. BASICS OF DIGITAL FILTERS : Fundamentals of digital filtering, various types of digital filters, design techniques of digital filters : window technique for FIR, bi-linear transformation and backward difference methods for IIR filter design, analysis of finite word length effects in DSP, DSP algorithm implementation consideration. Applications of DSP.

UNIT6. MULTIRATE DIGITAL SIGNAL PROCESSING:
Introduction to multirate digital signal processing, sampling rate conversion, filter structures, multistage decimator and interpolators, digital filter banks.

TEXT BOOKS :
1. Digital Signal Processing : Proakis and Manolakis; PHI
2. Digital Signal Processing: Salivahanan, Vallavaraj and Gnanapriya;TMH

REFERENCE BOOKS:
1. Digital Signal Processing: Alon V. Oppenhelm;PHI
2. Digital Signal processing(II-Edition): Mitra, TMH

NOTE: Eight questions are to be set - at least one from each unit. Students have to attempt five questions.

IC –417-E EMBEDED SYSTEM DESIGN LAB

L	T	P
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Class Work: 25
Exam: 25
Total: 50
Duration of Exam: 3 Hrs.

8051 Micro Controller

1. Write an Assembly language Programme (ALP) to generate 10kHz square wave.
2. Write an ALP to generate 10 kHz frequency using interrupts.
3. Write an ALP to interface one Microcontroller with other wring serial/parallel communication.
4. Write an ALP for temperature & pressure measurement & to display on intelligent LCD display

PIC Microcontroller

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5. Write an ALP for PWM based speed control of motor .
6. Write an ALP for PWM based regulator of voltage.
7. Write an ALP to send/receive the data from an computer to MC through serial communication

General

8. Study of Development tools/environment for Microcontroller Programme.
9. Develop an embedded system for traffic light controller using Micro controller
10. Develop an embedded system for the automatic motion of a car (Model of car) & Subsequent display on LCD using Microcontroller..

EE-421-E **DATA COMMUNICATION LAB**

L T P			
0 0 2	CLASS WORK	:	25
	EXAM	:	25
	TOTAL	:	50
	DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS:

1) To study different types of transmission media
2) To study Quadrature Phase Shift Keying Modulation.
3) To study Quadrature Amplitude Modulation.
4) To Study !6 Quadrature Amplitude Multiplexing.
5) To Study Serial Interface RS-232 and its applications.
6) To study the Parallel Interface Centronics and its applications.
7) To configure the modem of a computer.
8) To make inter-connections in cables for data communication in LAN.
9) To install LAN using Tree topology.
10) To install LAN using STAR topology.
11) To install LAN using Bus topology.
12) To install LAN using Token-Ring topology
13) To install WIN NT
14) To cofigure a HUB/Switch.

NOTE : 1. At least ten experiments have to be performed in the semester; At least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus .

EE-427-E **DIGITAL SIGNAL PROCESSING LAB**

L T P			
0 0 2	CLASS WORK	:	25
	EXAM	:	25
	TOTAL	:	50
	DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS:

Perform the experiments using MATLAB:

1. To represent basic signals (Unit step, unit impulse, ramp, exponential, sine and cosine).
2. To develop program for discrete convolution.
3. To develop program for discrete correlation.
4. To understand stability test.
5. To understand sampling theorem.
6. To design analog filter(low-pass, high pass, band-pass, band-stop).
7. To design digital IIR filters(low-pass, high pass, band-pass, band-stop).
8. To design FIR filters using windows technique.
9. To design a program to compare direct realization values of IIR digital filter
10. To develop a program for computing parallel realization values of IIR digital filter.
11. To develop a program for computing cascade realization values of IIR digital filter
12. To develop a program for computing inverse Z-transform of a rational transfer function.]

NOTE: At least ten experiments have to be performed in the semester; out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution.